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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/765,999	01/29/2004	Akira Okada	042063	3637
38834	7590	06/09/2005	EXAMINER	
WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP 1250 CONNECTICUT AVENUE, NW SUITE 700 WASHINGTON, DC 20036			TRAN, MAI HUONG C	
		ART UNIT	PAPER NUMBER	
			2818	
DATE MAILED: 06/09/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

AK

Office Action Summary	Application No.	Applicant(s)	
	10/765,999	OKADA ET AL.	
	Examiner	Art Unit	
	Mai-Huong Tran	2818	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 25 May 2005.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-11 is/are pending in the application.
 4a) Of the above claim(s) 9-11 is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-8 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 29 January 2004 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date 1/29/04.

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application (PTO-152)
 6) Other: _____.

DETAILED ACTION

Election/Restriction

Application's election without traverse of Group I (Claims 1-8) drawn to a semiconductor device is acknowledged for prosecution in the subject application. Accordingly, claims 9-11 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

Applicants have the right to file a divisional application covering the subject matter of the non-elected claims.

Drawings

The drawings are objected to for the following reasons.

Figures 1-3 are not designated by a legend such as "Prior Art". The Legend is necessary in order to clarify what applicant's invention is (see MPEP § 608.02g).

Claim Rejections - 35 U.S.C. § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1- are rejected under 35 U. S. C. § 102 (b) as being anticipated by U.S. Patent No. 5,949,655 to Glenn.

Regarding to claim 1, Glenn discloses a semiconductor device for fingerprint recognition, comprising a semiconductor chip 10 having a fingerprint recognition area for performing fingerprint recognition, a substrate 16 having an opening 19 that corresponds to the fingerprint recognition area, the semiconductor chip being flip chip bonded to the substrate 16 such that the fingerprint recognition area corresponds to the Opening 19, and an under-fill material 22 provided between the semiconductor chip 10 and the substrate 16 except for a position where the opening 19 is formed (col. 3, lines 54-67, col. 4, lines 1-36, and fig. 1.

Regarding to claim 2, the semiconductor device for fingerprint recognition wherein the substrate comprises a glass epoxy base material (col. 4, lines 21-27).

Regarding to claim 3, the semiconductor device for fingerprint recognition wherein the substrate comprises a polyimide resin base material (col. 4, lines 43-45).

Regarding to claim 4, the semiconductor device for fingerprint recognition wherein the substrate is a flexible substrate (col. 4, lines 21-27).

Regarding to claim 5, the semiconductor device for fingerprint recognition wherein the substrate is a TAB substrate (fig. 1).

Regarding to claim 6, the semiconductor device for fingerprint recognition wherein the substrate comprises an external connection terminal constituted by a solder ball (col. 4, lines 1-27, fig. 1).

Regarding to claim 7, the semiconductor device for fingerprint recognition wherein the substrate comprises an external connection terminal constituted by a connector (fig. 1, col. 4, lines 53-66).

Regarding to claim 8, the semiconductor device for fingerprint recognition wherein the semiconductor chip performs fingerprint recognition using the electrostatic-capacity principle, and by a finger sweeping across the fingerprint recognition area (fig. 1).

Conclusion

Any inquiry concerning this communication on earlier communications from the examiner should be directed to Mai-Huong Tran, (571) 272-1796. The examiner can

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normally be reached on Monday-Thursday from 8:00 AM to 6:30 PM. The examiner's supervisor, David Nelms can be reached on (571) 272-1787.

The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Mai-Huong Tran